

ABSTRACT

A method for communicating between a controller and a device with double-buffered
5 inputs comprises the steps of providing one or more communication paths for exchanging data
between the controller and the device, providing a data transfer control signal from the controller
to the device for transferring input data from one or more input registers into one or more
latchable data registers, and providing a data transfer delay signal from the device to the
controller, wherein, in a first logic state, the data transfer delay signal prevents transfer of input
10 data from the input registers into the latchable data registers until after a transition to a second
logic state occurs on the data transfer delay signal. An apparatus for communicating between a
controller and a device is also described.

4 11 22 33 44 55 66 77 88 99 100 111 122 133 144 155 166 177 188 199 200 211 222 233 244 255 266 277 288 299 300 311 322 333 344 355 366 377 388 399 400 411 422 433 444 455 466 477 488 499 500 511 522 533 544 555 566 577 588 599 600 611 622 633 644 655 666 677 688 699 700 711 722 733 744 755 766 777 788 799 800 811 822 833 844 855 866 877 888 899 900 911 922 933 944 955 966 977 988 999 1000